

ERU chokes

ERU 27, SMD flat wire high current inductors

Series/Type: B82579A*A027

Ordering code:

Date: September 2023

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SMD flat wire high current inductors

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Construction

- Isolate alloy powder core
- Magnetically shielded
- Helical winding
- Self-leaded construction
- Under body termination
- 3 pins for improved reliability

Features

- High rated current
- Extremely low DC resistance
- Very low profile and smallest possible footprint
- Suitable for pick-and-place process
- RoHS-compatible
- Easily customized
- Qualified to AEC-Q200

Applications

Energy storage chokes for

- DC-DC converters
- VRM modules
- POL converters
- Solar converters

Terminals

■ Lead-free tinned

Assembly

Additional mechanical fixation should be applied

Remark

To keep the maximum limited component temperature the device must be efficiently connected to the cooling system.

Marking

Manufacturer, ordering code, date code / production identification code

Delivery mode

Blister tape



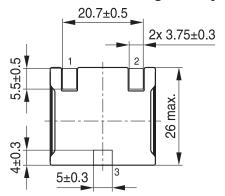


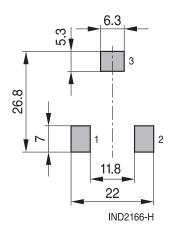
IND2165-G-E

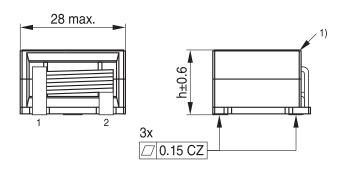
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Dimensional drawing and layout recommendation







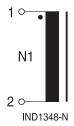
1) Chamfer (w/o) on the core edges allowed

Size ISO 14405 (E) All dimensions in mm

Part tolerances to ISO 2768-cL / ISO 8015.

IND1276-L-E

Circuit diagram





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Technical data and measuring conditions

specified @ 25°C if not mentioned otherwise, all values without tolerance are typical values

Inductance L _R	Measured at 100 kHz, 0.1 V, +25 °C
Inductance tolerance	±15%
Saturation current I _{Sat, 100 °C}	Current that will result in an approximately 20% drop in the inductance values at the specified temperature.
Rated current I _R	Current that will cause a ∆40K self-heating at room temperature
DC resistance R _{DC}	Measured at +25 °C, tolerance ±10%, typical values
Self-resonant frequency	> 2 MHz
High voltage: N1 – core	200 V DC, 1 s
Solderability (test of wettability of the pins)	(245 \pm 5) °C, (3 \pm 0.3) s, wetting of soldering area \geq 95% (based on IEC 60068-2-58, solder bath method)
Resistance to soldering heat	To JEDEC J-STD 020E (Tc: +245 °C on pin)
Operating temperature range	-40 °C +150 °C (component)
Storage conditions (packaged)	-25 °C +40 °C, ≤ 75% RH

Characteristics and ordering codes

$\overline{L_R}$	I _{Sat,100°C}	I _R	R _{DC}	Height h	Approx.	Internal code	Ordering code
			(typ.)	(nom.)	weight		
μΗ	Α	Α	mΩ	mm	g		
2.3	85	48	0.68	14.1	42	B82579A1232A027	ERU27M-2R3L
3.9	68	46	0.88	15.5	47	B82579A1392A027	ERU27M-3R9L
6.2	55	37	1.39	15.2	47	B82579A1622A027	ERU27M-6R2L
8.5	46	36	1.66	16.4	50	B82579A1852A027	ERU27M-8R5L

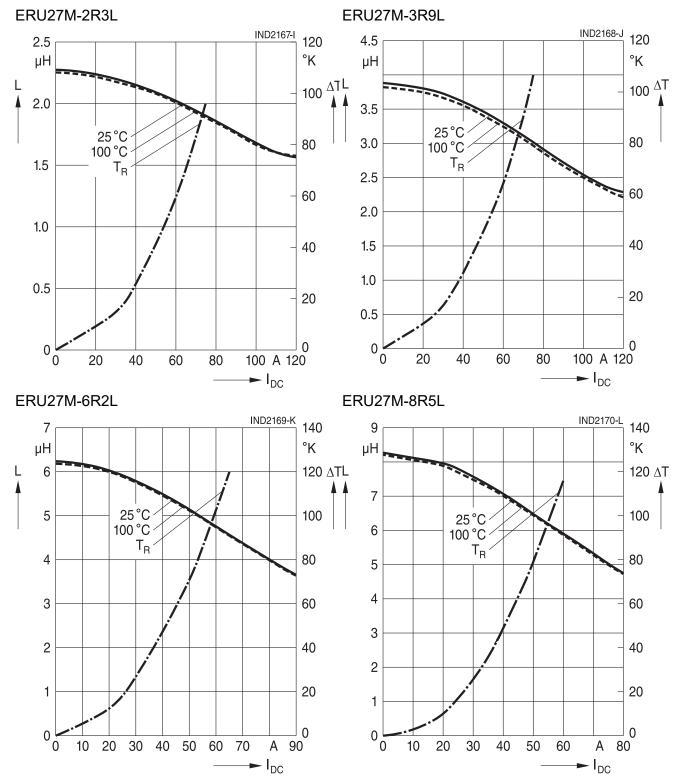


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Inductance L versus DC load current IDC

The temperature rise ΔT is measured at an ambient temperature of +25 °C. A current is applied for 30 minutes and the temperature is measured on top of the inductor which is mounted on a printed circuit board. No forced air cooling is applied. The inductance vs current curves are generated by measuring the inductor at +25 °C and +100 °C.



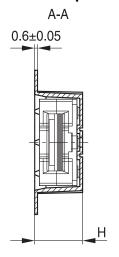


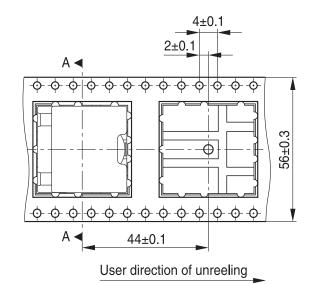
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Taping and packing

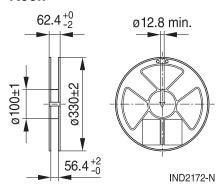
Blister tape:





IND2171-M-E

Reel:



	Packing unit	
component h	cavity H (blister tape)	pcs. per reel
nom.	max.	
14.1	15.0	80
15.5	16.4	75
15.2	16.4	75
16.4	17.3	65



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Cautions and warnings

- Please note the recommendations in our Inductors data book (latest edition), online catalogs and in the data sheets.
 - Particular attention should be paid to the derating curves, if given. Derating applies in the case the ambient temperature in application exceeds the rated temperature of the component.
 - Ensure the operation temperature of the component in application not to exceed the maximum specified value or the upper climatic category temperature.
 - The soldering conditions should also be observed. Temperatures quoted in relation to wave soldering refer to the pin, not the housing.
- If the components are to be washed varnished it is necessary to check whether the washing varnish agent that is used has a negative effect on the wire insulation, any plastics that are used, or on glued joints. It is possible for washing varnish agent residues to have a negative effect in the long-term on wire insulation.
 - Washing processes may damage the product due to the possible static or cyclic mechanical loads (e.g., ultrasonic cleaning). They may cause cracks to develop on the product and its parts, which might lead to reduced reliability or lifetime.
- The following points must be observed if the components are potted, sealed, or varnished in customer applications:
 - Many potting, sealing, or varnishing materials shrink as they harden. They therefore exert a
 pressure on the plastic housing or core. This pressure can have a deleterious effect on electrical properties, and in extreme cases can damage the core or plastic housing mechanically.
 - It is necessary to check whether the potting, sealing or varnishing materials used attack or destroy the wire insulation, plastics, or glue.
 - The effect of the potting, sealing, or varnishing materials may change the high-frequency behavior of the components.
- Magnetic core materials such as ferrites are sensitive to direct impact. This can cause the core material to flake or lead to breakage of the magnetic core material.
- Any type of tension or pressure on the product may result in damage and affect its functionality and reliability.
 - The products are only to be attached to fixings or mounting holes provided for this purpose in accordance with the data sheet.
 - If additional mechanical forces are applied to the component, e.g., application of gap pads, it
 is necessary to check whether they attack or destroy any part of the component.
 - It is not permitted for the product specified in the data sheet to assume a mechanical function in the final application.
- Inductance value can drop if external metallic or magnetic parts will be put close to the coil or into the air gap of the coil or core or magnetic material.
- Even for customer-specific products, conclusive validation of the component in the circuit can only be carried out by the customer.

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